

State of the IO

Wolfram Sang

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Upcoming tasks (upporting bsp392)

Geert

- \$SPI, SCIF upporting (base)

Kaneko-san

- Thermal upporting (base)

Marek

- PCIE upporting (base)
- occasional SDHI? (base)

Niklas

- SDHI upporting (base)
- HS400 manual calibration (add)

Upcoming tasks (upporting bsp392 II)

Shimoda-san

- PWM, USB upporting (base)

Simon

- RAVB upporting (base)

Uli

- SCIF upporting (base)
- (D3 upporting?)

Wolfram

- I2C, SDHI (base)

Upcoming non-upporting tasks

- group leading (Wolfram, add.)
- IIC refactoring (?)
- “I2C address pools” (add. task?)
- ?RAVB: configure MTU while interface is up
- ?SDHI: eMMC HighPriorityInterrupts (HPI)
- ?32bit DMA limitation
- whatever bug Renesas finds

Thanks for listening!